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|---------------------------------|---|----------|----------|-------------|----------|
| U.S. UTILITY Patent Application |   |          |          |             |          |
| JG3<br>897                      |   | O.I.P.E. |          | PATENT DATE |          |
| MA                              |   | AK       |          | 2004        |          |
| SCANNED UKM4-a-A 2004           |   |          |          |             |          |
| UKM2                            |   |          |          |             |          |
| APPLICATION NO.                 | CONT/PRIOR  | CLASS    | SUBCLASS | ART UNIT    | EXAMINER |
| 09/961114                       | D   | 439      | 713      | 2833        | Aguero   |
| APPLICANTS                      | Donald Perino<br>Wayne Richardson<br>John Dillon                    |          |          |             |          |
| TITLE                           | Chip socket assembly and chip file assembly for semiconductor chips |          |          |             |          |
| 317<br>-353- X or T             |   |          |          |             |          |
| PTO-2040<br>12/99               |   |          |          |             |          |

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| TERMINAL<br>DISCLAIMER   | DRAWINGS     |             |                              | CLAIMS ALLOWED             |                      |        |
|--|--------------|-------------|------------------------------|----------------------------|----------------------|--------|
|  | Sheets Drwg. | Figs. Drwg. | Print Fig.                   | Total Claims               | Print Claim for O.G. |        |
| <input type="checkbox"/> The term of this patent<br>subsequent to _____ (date)<br>has been disclaimed.                       |              |             |                              | NOTICE OF ALLOWANCE MAILED |                      |        |
| <input type="checkbox"/> The term of this patent shall<br>not extend beyond the expiration date<br>of U.S. Patent. No. _____ |              |             |                              |                            |                      |        |
| _____  | _____        | _____       | (Assistant Examiner)         |                            |                      | (Date) |
| <input type="checkbox"/> The terminal _____ months of<br>this patent have been disclaimed.                                   |              |             |                              | ISSUE FEE                  |                      |        |
| _____  | _____        | _____       | (Primary Examiner)           |                            |                      | (Date) |
| _____  | _____        | _____       | (Legal Instruments Examiner) |                            |                      | (Date) |
| Amount Due   |              | Date Paid   |                              |                            |                      |        |
| ISSUE BATCH NUMBER   |              |             |                              |                            |                      |        |

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